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**Complete if Known**

Application Number	10/765,531
Filing Date	01/27/2004
First Named Inventor	Burn Jeng Lin et al.
Art Unit	1756
Examiner Name	Stephen D. Rosasco
Attorney Docket Number	N1085.00225 (N1280-00235)

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	A	MUTSUNORI IGARASHI ET AL., "A Diagonal-Interconnect Architecture and Its Application to RISC Core Design", ISSCC 2002, Session 12a, 3 pages	

Examiner Signature		Date Considered	9/06
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\*EXAMINER: Initial reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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